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Power Controlled Coded-Sequence Self-Encoded Spread Spectrum Communications

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Abstract—In this paper, we propose to apply power control to coded-sequence self-encoded spread spectrum (CS-SESS) communications in dynamic fading channels. We determine the statistical characteristics of the power enhancement in the transmitter due to power control and show that the proposed approach improves the power efficiency in mobile cellular systems.

In addition, we propose to mitigate self interference in CS-SESS by exploiting the information that the transmitter obtains, through feedback training sequences, about the de-spreading code in the receiver. The results show that the mitigation is significant even with only selective training chips from the de-spreading sequence. Similar improvement can be achieved with feed-forward training sequences to the receiver about the spreading code in the transmitter.

Index Terms—spread spectrum, power control, iterative detection, coded-sequence self-encoded spread spectrum.

I. INTRODUCTION

Coded-sequence self-encoded spread spectrum derives its spreading codes from the random information source rather than using traditional pseudo-random codes. In recent work, we have shown that with iterative detection, the memory in CS-SESS modulated signals not only can deliver a 4.7dB gain in AWGN channels, but also can be exploited to achieve time diversity and robust bit-error rate (BER) performance in fading channels [3]. In this paper, transmitter precoding, as an alternative to combat the channel dynamic of CS-SESS communications in dynamic fading channel is proposed and analyzed. Transmitter precoding was originally proposed for additive white Gaussian noise (AWGN) channels [4, 5] and independently for flat fading channels in [6].

Although, CDMA based systems provide high power efficiency and moderate error rates, coherent modulation does not provide reliable communication on fading channels if the impulse responses of these channels are not known. Traditionally, channel estimation can be achieved by periodical transmission of known training sequences or using pilot channel. Generally, for better estimation accuracy, more training symbols or higher power for pilot channel shall be required [1]. In this paper, we exploit the training sequence in CS-SESS communication to combat the self interference that attenuate the strength of the de-spread signal at low SNR. We study three scenarios

1) Transmitter exploits the de-spreading code of the receiver obtained through feedback training sequence
2) Receiver exploits the spreading code of the transmitter obtained through feedforward training sequence
3) Receiver corrects the most significant de-spreading sequence errors by exploiting both feed forward and feedback training sequence.

We showed that the mitigation of self interference is significant in all three cases.

The rest of the paper is organized as follows. In section II, we describe the CS-SESS scheme with dynamic fading channel. Power control to combat channel dynamic is explained in section III with the BER performance results. In section IV, we explain three scenarios that exploit the training sequence to mitigate self interference and showed its numerical results followed by conclusion in section V.

II. SYSTEM MODEL

A. Transmitter

Fig. 1 illustrates the schematic block diagram of the proposed CS-SESS. T is the bit duration and Te is the chip duration. The source information b is assumed to be bipolar values. These symbols are stored sequentially in 2N delay
shift registers. The delayed symbols are then encoded by means of modulo-2 adders to generate the spreading sequence $s_k$ of length $N$ that modulate the current symbol at the chip rate $N/T$. For example, the spreading sequence for the $k^{th}$ bit $b_k$ is given as

$$s_k = \begin{bmatrix} b_{(k-1)} \oplus b_{(k-2N)} \\ b_{(k-2)} \oplus b_{(k-2N+1)} \\ \vdots \\ b_{(k-N)} \oplus b_{(k-N-1)} \end{bmatrix}$$ (1)

Thus, with a random input data stream, the sequence is also random and time-varying from one bit to another. The spread chips of bit $b_k$ are then transmitted as

$$c_k = b_k s_k$$ (2)

If $c_{kn}$ is the $n^{th}$ chip of spread sequence $c_k$ and $s_{kn}$ is the $n^{th}$ chip of spreading sequence $s_k$ then

$$c_{kn} = b_k s_{kn} = b_k b_{(k-n)} b_{(k-2N-1+n)}$$ (3)

Note that the XOR operator is omitted in (3) because of its similarity to multiplication in bipolar case.

B. Dynamic Rayleigh Fading Channel

The Rayleigh fading can be expressed using two random variables $a$ and $b$ as

$$h = \frac{a^2 + b^2}{\sqrt{2}}$$ (4)

We assume the channel is dynamic by the variance $X$ which varies from 0 to 2. Dynamic Rayleigh fading can be expressed as

$$h = \sqrt{X} \left[ \frac{a^2 + b^2}{\sqrt{2}} \right]$$ (5)

C. Correlation Detection

The receiver performs the reverse operation for symbol recovery by means of a correlation detector. If $\tilde{c}_{kn}$ is the received signal

$$\tilde{c}_{kn} = h_k c_{kn} + \zeta_{kn}$$ (6)

where the additive Gaussian noise $\zeta_{kn}$ has the zero mean and variance $N N_0/2$. Note that the variance is narrow-band noise variance $N_0/2$ multiplied by the spreading factor $N$, then the correlation estimate $\tilde{b}_k$ of bit $b_k$ is given as

$$\tilde{b}_k = \tilde{c}_k \tilde{s}_k = \sum_{n=1}^{N} \tilde{c}_{kn} \tilde{s}_{kn}$$ (7)

where $\tilde{s}_{kn}$ is the $n^{th}$ de-spreading chip for $k^{th}$ bit generated by appropriately modulo-2 adding the delay registers. We assume that the content of the delay registers in transmitter and receiver should be identical at the start of the data transmission. Notice that the initial synchronization and a complete knowledge of the delay-register structure could make data recovery difficult for an unintended receiver. By substituting (6) and (3) into the correlation estimate given in (7), we have

$$\tilde{b}_k = h_k \left[ \sum_{n=1}^{N} b_{(k-n)} b_{(k-2N-1+n)} \tilde{b}_{(k-n)} \tilde{b}_{(k-2N-1+n)} \right] + \sum_{n=1}^{N} \zeta_{kn} \tilde{s}_{kn}$$ (8)

The content of delay registers are then updated by the hard decision of the correlation estimate $\tilde{b}_k$.

$$\tilde{b}_k = \text{sgn}(\tilde{b}_k) = \begin{cases} 1, & \text{if } \tilde{b}_k > 0 \\ -1, & \text{if } \tilde{b}_k < 0 \end{cases}$$ (9)

Since there may be errors in correlation estimate, the re-constructed de-spreading sequence could be different from the spreading sequence at the transmitter. A bit error therefore will propagate through the delay registers for the next 2N bits instead of N bits in SESS and cause self interference (expressed by the term inside the square bracket in (8)) that attenuates the strength of the despread signal.

D. Iterative Detection

The iterative detector, like the MLSE detector, also improves the system performance by exploiting a sequence of received signals. However, like the feedback detector, the complexity of the iterative detector is linear with $N$.

We begin by expressing the spread sequence $c_k$ of $2N+1$ bits $b_1, b_2 ... b_{(2N+1)}$ in a matrix as shown in (10). Based on (3) the spread chips can be written in $2N+1 \times N$ block, since $N$ is the spreading length. Thus, each element of right hand side matrix of equation (10) represent a chip and $j^{th}$ row includes the chips of bit $b_j$ (also can be expressed as chips $c_{j1}, c_{j2} \cdots c_{jN}$). In the transmitter, row by row each spreading sequence is transmitted.

In the receiver, $\tilde{c}_k$ of $c_k$ is received. We begin by decoding the first bit $b_1$. Observe from the transmitted chips of (10), the current decoded symbol $b_1$ not only depends upon the previous $2N$ symbols $(b_0, b_1 \cdots b_{(2N-1)})$ as indicated in the first row of (10), which can be decoded using correlation detection. But $b_1$ also can be re-estimated from future $2N$ chips $(c_{21}, c_{32} \cdots c_{(N+1)N}, c_{(N+2)N} \cdots c_{(2N+1)N})$ of (10) from $2N$ future transmitted symbols $(c_2, c_3 \cdots c_{(2N+1)})$ respectively. This re-estimation of bit from $2N$ future symbols can be termed as time diversity detection. This is due to the fact that bit $b_k$ stays in the delay register for next 2N bits $(b_{(k+1)}, b_{(k+2)} \cdots b_{(k+2N)})$ spreading. Also note from (10), the time diversity estimate of $b_1$ requires previous iterated bits $(b_{(-2N+2)}, b_{(-2N+4)} \cdots b_0)$ and also future correlation detected bits $(b_2, b_3 \cdots b_{(2N+1)})$.

Thus, the time diversity estimate of $b_1$ from $2N$ future symbols can be used to improve the estimate of the current symbol $b_1$ from the correlation detection. If $\tilde{c}_{kn}$ is the estimated value of $c_{kn}$ at the receiver, the iterative detection soft estimate $\tilde{\tilde{b}}_k$ can be obtained by adding the correlation and time diversity estimates as shown in (11). The output of the first iteration is
\[
\begin{bmatrix}
  c_1 \\
  c_2 \\
  \vdots \\
  c_{(N+1)} \\
  c_{(N+2)} \\
  \vdots \\
  c_{(2N+1)}
\end{bmatrix}
= 
\begin{bmatrix}
  b_1 b_0 b_{(-2N)} & b_1 b_{(-2N+2)} & \cdots & b_1 b_{(-N)} b_{-N} \\
  b_2 b_1 b_{(-2N+2)} & b_2 b_0 b_{(-2N+3)} & \cdots & b_2 b_{(-2N)} b_{(-N+1)} \\
  \vdots & \vdots & \ddots & \vdots \\
  b_{(N+1)} b_N b_{(-1-N)} & b_{(N+1)} b_{(-N+1)} b_{(-N+2)} & \cdots & b_{(N+1)} b_1 b_0 \\
  b_{(N+2)} b_{(N+1)} b_{(2-N)} & b_{(N+2)} b_N b_{(-N+3)} & \cdots & b_{(N+2)} b_2 b_1 \\
  \vdots & \vdots & \ddots & \vdots \\
  b_{(2N+1)} b_{(2N)} b_1 & b_{(2N+1)} b_{(2N-1)} b_2 & \cdots & b_{(2N+1)} b_{(N+1)} b_N
\end{bmatrix}
\tag{10}
\]

then obtained by the hard decision of \( \tilde{b}_k \). It is clear that the number of chips employed by the above iteration in making a bit decision is 3N chips instead of 2N chips in SESS. As a result, an improvement of system performance can be reasonably expected. Since 2N future symbol decisions are needed for an iteration of the current symbol, there will be a delay of 2N symbols for each iteration.

### III. CS-SESS POWER CONTROL

#### A. Power Control

Precoding is done to combat the channel dynamics. The crucial assumption for precoding is that the transmitter has information about the channel between it and the active receiver. This information can be obtained from the receiver via feedback channel or can be estimated at the transmitter when a time-division duplex mode is employed, that is, when the same frequency band is employed for transmit and receive direction. Another important requirement is that channel is sufficiently slow that it remains essentially constant over the bit duration. We apply the power control factor \((1/\sqrt{X})\) at the transmitter as

\[
c_k = \frac{1}{\sqrt{N}} b_k s_k \tag{12}
\]

The Fig. 2 shows the histogram of the power control variable. It is clear from the figure that most of the values are concentrated between 0.5 and 10.

The mean and variance of power control variable can be found as

\[
E\left[\frac{1}{\sqrt{X}}\right] = 3.03 = 4.8\text{dB}
\]

\[
Var\left[\frac{1}{\sqrt{X}}\right] = E\left[\frac{1}{\sqrt{X^2}}\right] - E\left[\frac{1}{\sqrt{X}}\right]^2 = 163.885 = 22\text{dB} \tag{13}
\]

#### B. Results

The simulations were done for \( N=64 \) unless otherwise specified. Fig. 3 shows the performance of correlation detector of Fig. 1 under dynamic fading channel with and without power control. The plot shows that the performance gain of \( \sim 5\text{dB} \) at \( 10^{-2} \) BER has been achieved with power control. As we discussed, the degradation at lower SNR is caused by error propagation, such that the bit errors effectively contribute to an additional attenuation of the de-spread signal. Also note that the performance becomes similar to that of BPSK of Rayleigh fading channel for \( \text{SNR} > 10\text{dB} \) by fully eliminating the channel dynamics.

Fig. 4 shows the BER performance of iterative detector with and without power control in dynamic fading channel. It can
be seen that power control can extract additional gain of about 1.5dB at $10^{-2}$ BER. Performance degradation at lower SNR is because of its nature to use correlation detector output to re-estimate the current bit. A comparison of the plots in Figs. 3 and 4 further demonstrates that iterative detector can attain performance gain of $\sim 17$dB at $10^{-3}$ BER with power control.

IV. CS-SESS WITH TRAINING SEQUENCE

Training sequence is often used in wireless communication systems to estimate the slowly time-varying channel. Here we propose three scenarios to mitigate the self interference in CS-SESS by exploiting the

1) Feedback training sequence to convey de-spreading code of the receiver to the transmitter
2) Feed-forward training sequence to convey spreading sequence of the transmitter to receiver
3) Both feedback and feed-forward training sequence to communicate between transmitter and receiver

We assume that these high power training sequence are error free.

A. Feedback Training Sequence

We assume that the transmitter’s delay registers are updated by the de-spreading sequence of the receiver, obtained through the feedback training sequence. Fig. 5 shows the performance of the correlation detection with and without using the feedback training sequence in Rayleigh fading channel. Simulations results were obtained for exploiting feedback training sequence once every bit, $N/2$ or $N/4$ bit transmissions. The plot shows that the self interference can be eliminated completely at lower SNR. Also note that the performance of receiving feedback training sequence, every $N/2$ or $N/4$ bit transmissions is also almost similar to BPSK due to the fact that updated de-spreading code in the delay registers of the transmitter stays until it is flushed out.

Fig. 6 shows the performance of iterative detection with and without using the feedback training sequence. The result shows that the performance is same as the correlation detector with no additional time diversity gain of iterative detection. This is due to the loss of original source information in the transmitter delay register to spread the future $2N$ bits which can be used later in the receiver to re-estimate the current bit using time diversity detection.

1) Modified Method For Feedback Training Sequence: Above method of using the de-spreading sequence helps the correlation detection but it eliminates the use of iterative detection. So we proposed an modified method with below algorithm

1) Get the de-spreading sequence from the receiver once every bit
2) Compare with spreading sequence
3) If Number of disagreed chips ≥ N/4
   a) Then flip the sign of the each spread sequence chips until number of disagreed chips is ≤ N/4
   b) Don’t change the values of the delay registers of the transmitter

4) Else follow the regular the spreading process
This method will not change every error chips to coincide with de-spreading sequence instead it leaves N/4 of the wrong chips intact, which in-turn helps iterative detection, by preventing the lose of N/4 original source chips.

CS-SESS Performance using the modified method is shown in Fig. 7. Although, correlation detection performance is not similar to BPSK fading, this method clearly helps for iterative detection. It is clear that iterative detection is able to attain gain of ~1dB at 10^{-4} BER beyond eliminating the self-interference in low SNR.

B. Feed-forward Training Sequence

We assume that the receiver can obtain the spreading sequence of the transmitter through feed-forward training sequence. Fig. 8 shows the BER performance with and without utilizing the feed-forward training sequence to update the receiver with the spreading sequence for every 2N, 4N or 6N bit transmissions, which is 50%, 66% or 75% throughput respectively. It is clear that using feed-forward training sequence eliminates the self-interference depending upon the frequency of the update. Note that the update every 2N bit transmissions can eliminate the self-interference completely. A comparison of Figs. 7 and 8 further demonstrate that iterative detection using feed-forward training sequence with less frequent update of every 6N bit transmission not only have higher bandwidth efficiency but also provides significant performance improvement.

C. Feedback and Feed-forward Training Sequence

Sending the whole spreading or de-spreading sequence often in training sequence is not practically possible, so we tried to send the location of the 4, 6 or 8 most significant mismatched delay register bits in the de-spreading sequence for every 2N bit transmission. We assume that transmitter obtains the de-spreading chips of the receiver through feedback training sequence to compare and send the delay register error locations. Receiving the error bit locations, the receiver can invert their values to match with spreading sequence.

Fig. 9 shows the performance of CS-SESS for 4, 6 and 8 error corrections in the receiver. Correcting 4 and 6 errors in the total of 64 delay registers does not show any performance improvement. Correcting 8 error in the de-spreading sequence shows a 1dB improvement in self-interference.
V. CONCLUSION

In this paper, we proposed the transmitter precoding to combat the channel dynamics of CS-SESS communication. Simulations showed that the performance gain of 5dB and 1.5dB for SNR>6dB can be achieved with power control in correlation detection and iterative detection respectively in dynamic fading channel.

In addition, performance of CS-SESS was studied under three scenarios of exploiting training sequence to mitigate the self-interference in Rayleigh fading channel. The results showed that using feedback training sequence can eliminate the self-interference in correlation detection. The proposed modified method to use feedback training sequence not only eliminates the self-interference in low SNR of correlation detection but helps iterative detection to attain additional 1dB gain compared to original scheme. Exploiting feed-forward training sequence to obtain the spreading chips of the transmitter in the receiver can improve the performance significantly for less frequent training sequence by increasing the bandwidth efficiency.

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