Series-Connected Self-Reconfigurable Multicell Battery

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Series-Connected Self-Reconfigurable Multicell Battery

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Abstract—This paper proposes a novel series-connected, self-reconfigurable, multicell battery design. A new cell switching circuit topology is proposed and implemented by using high-efficiency semiconductor devices for dynamic configuration of the multicell battery. Each cell in the battery only uses two switches to fully control its operation independently. The proposed design can maximally utilize the battery’s capacity and is tolerant to failures of single or multiple cells. These prolong the operating time and lifespan and enhance the reliability of the battery system. By using the proposed design, additional monitoring, control, protection and optimization functions can be readily added to each cell and the battery system to produce a smart battery. The proposed design is validated by simulation and experimental results for a six-cell polymer lithium-ion battery pack. The proposed design is universal and can be applied to any type and size of battery cells.

I. INTRODUCTION

The traditional multicell battery design usually employs a fixed configuration to connect multiple cells in series during operation in order to achieve the required voltage level [1]. However, cell state variations are commonly present in series-connected multicell batteries. In this case, the fixed-configuration design can only utilize a part of the total battery capacity, which reduces the operating time and lifespan of the battery system. Moreover, the fixed configuration has low reliability and low fault tolerance because the failure of any single cell during operation will result in the failure of the whole battery system.

A commonly used method to solve the problem of cell state variations is using a cell balancing circuit. However, most existing balancing circuits use dissipative resistors, resulting in energy loss [2]. The latest products of cell balancing integrated circuits (ICs) [3] use electronic converters to transfer charge from cell to cell during operation. However, this solution increases the cost and volume of the battery system. Moreover, most of these cell balancing ICs were developed for small multicell batteries in portable electronic devices. No such circuits are available for large-scale battery systems as yet. The state-of-the-art batteries have safety circuits to protect them from high temperature, overcharge, and over-discharge by monitoring the temperature, voltage, and current of each cell. However, lacking an effectively reconfigurable topology, the safety circuits will cut off the whole battery system when any single cell is operated in these abnormal conditions. Recently, several reconfigurable multicell battery topologies have been proposed for portable electronic devices [4]-[6]. However, these topologies are too complex and unrealistic for the battery systems with large numbers of cells.

This paper proposes a new series-connected, self-reconfigurable, multicell battery design. A new cell switching circuit topology is proposed where each cell can be controlled independently and only uses two switches to fully control its charge, discharge, and cutoff. The resulting battery system can be operated with variable terminal voltages to reduce losses in converters [6]. The cell switching circuit and the control circuit for each cell are designed and implemented by using high-efficiency semiconductor devices. Compared to existing reconfigurable battery designs, the number of switches in the proposed design is significantly reduced. This reduces the cost, complexity, and control effort of the battery system and facilitates the reconfiguration process. By controlling the switches, each cell can operate independently with different charge/discharge methods. For example, through a pulsed discharge method, the recovery effect can be effectively utilized to improve the discharge efficiency of the battery [7]-[9]. The proposed design can maximally utilize the battery’s capacity, thereby maximizing the operating time of the battery system. Moreover, the proposed design is tolerant to failures of single or multiple cells, thereby enhancing the reliability of the battery system. The proposed design is validated by simulation and experimental studies for a six-cell polymer lithium-ion battery pack. The proposed design is universal and can be applied to any type and size of battery cells.

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II. PROPOSED SERIES-CONNECTED, RECONFIGURABLE, MULTICELL BATTERY DESIGN

The proposed design consists of three parts: (1) a series cell array, (2) a cell switching circuit, and (3) a control circuit, as shown in Fig. 1.

A. Series Cell Array

The nominal voltages of most single rechargeable battery cells are limited to several volts, e.g., 3.7 V for the polymer lithium-ion battery cells used in this paper, which are much lower than the voltages required in many applications. Fig. 1 shows a series cell array in which all battery cells are connected in series by the cell switching circuit to meet the voltage required for certain applications. Moreover, multiple series cell arrays can be connected in parallel to meet a higher current requirement, which however is not within the scope of this paper.

B. Cell Switching Circuit

Fig. 2 shows the proposed topologies for the cell switching circuit used for connecting/disconnecting the cells and reconfiguration of the series cell array. Only 2n controllable power switches are needed to form the cell switching circuit for an array with n cells in the topology. Each cell uses two controllable power switches, e.g., the switches Sk1 and Sk2 for Cell k (k = 1, ..., n), which turn on/off alternatively to connect/cut off the cell from the array. The cell switching circuit is designed to meet the following specifications to make the battery pack reconfigurable during the operation. First, the cell switching circuit can connect any cell in series with others in the array if needed. For example, if Cell k needs to be connected, then Sk1 turns on and Sk2 turns off. When Cell k is connected, it should be able to operate in both charge and discharge modes, which is controlled by Sk1. Second, the cell switching circuit can cut off any single or multiple cells from the array if needed. For example, if Cell k needs to be cut off from the array to prevent it from overcharge in the charge mode or from over-discharge in the discharge mode, then Sk1 turns off and Sk2 turns on. Turning on Sk2 ensures that Cells k+1, ..., n can be connected in the array to supply (discharge) or store (charge) energy through the terminals of the battery pack. Therefore, these two specifications require all switches to conduct current in two directions. Additionally, each switch should have a negligible conduction loss so that the reconfigurable capability is not obtained at a cost of unacceptably reduced battery efficiency. In this application, switching losses are not a concern because it typically takes a long time, e.g., 30 minutes or longer, for a switch to change its state. High-efficiency power MOSFETs are a good candidate for implementation of the switches in the cell switching circuit. The power MOSFETs can conduct bi-directional currents and have a negligible conduction loss because of their negligible on resistance, not to mention low cost. In the proposed design, it is important to connect the MOSFETs in the correct direction. The anode of body diode of MOSFET Sk1 should be connected to the negative terminal of the cell k. This connection can block the unwanted discharges of the cell k. On the other hand, the cathode of body diode of MOSFET Sk2 should be connected to the positive terminal of the cell, which prevents the unwanted charges flowing through the body diode of the power MOSFET to Cells k+1, ..., n when Cell k is connected in the array, i.e., Sk1 is on.

Fig. 3 illustrates a switch implementation for Cell k (k = 1, ..., n) using an n-channel power MOSFET (Sk1) and a p-channel power MOSFET (Sk2) with the gate drive circuit and control module. The gate drive circuit is implemented by using four low-cost, small-signal BJTs, which only use the voltage of Cell k; no additional voltage source is required. When Qk1 turns on, it drives Qk2 on, which turns on the power MOSFET Sk1 by using the voltage of Cell k. Turning off Sk1 is accomplished by Qk3, which discharges the parasitic capacitor between the gate and source terminals of Sk1 when Qk1 turns off. Sk2 is turned on when Qk4 turns on, which provides a gate signal to charge the parasitic capacitor between the source and gate terminals of Sk2 to turn on the p-channel MOSFET Sk2. Two 15-V Zener diodes, Dzk1 and Dzk2, are used to prevent the high-side voltage of the gate.
drive exceeding the maximum value of $V_{gs}$ of Sk1 and the maximum value of $V_{gs}$ of Sk2, respectively. In this switching implementation, an n-channel MOSFET with a low threshold $V_{gs}$ (e.g., 1.5–2 V) should be used for Sk1. The selection of the components in the gate drive circuit ensures that the energy consumption of the gate drive circuit is negligible compared to the energy flow in the cell and the transient switching period is minimized to prevent short circuit between Sk1 and Sk2. In [10], the authors have proposed a cell switching implementation, which however requires an additional voltage source for the gate drive circuit. Since the gate drive circuit shares the same ground with the MOSFET switching circuit and due to the voltage limitation of the small-signal BJTs, the application of the switching implementation in [10] is limited to low-voltage (e.g., < 60 V) battery systems only. Compared to the switching implementation in [10], the new switching implementation in Fig. 3 removes the need of an additional voltage source for the gate drive circuit. Therefore, the new switching implementation can be used for multicell battery systems at any voltage levels.

C. Control Module

The control circuit is designed to generate signals to control on/off of the power MOSFETs. Each cell has a control module, which generates gate signals for two small-signal BJTs, Qk1 and Qk4, to drive the corresponding power MOSFETs, as shown in Fig. 3. The control modules of all the cells form the control circuit in Fig. 1. Fig. 4 shows the schematic diagram of a control module, which consists of a monitoring circuit, comparators, and a signal generator. The monitoring circuit performs the functions, such as monitoring the voltage, current and temperature, tracking the state of charge (SOC), etc., for each cell. A basic function of the control circuit in this paper is to protect the battery cells from overcharge and over-discharge during operation. Fig. 5 shows the voltage monitoring circuit for one cell, which uses differential op-amps and high-precision (0.1%) resistors to generate the input signal (i.e., the cell operating voltage) for the comparators in Fig. 4. The voltage comparators compare the operating voltage of the cell with the designated discharge and charge cutoff voltages, $V_{\text{cutoff}}$ and $V_{\overline{\text{over}}}$ respectively, to protect the cell from overcharge and over-discharge during operation. The outputs of the comparators indicate the operating states of the cell and are used by the signal generator to generate the gate signals to turn on/off the two small-signal BJTs, Qk1 and Qk4. Additional monitoring, control, protection and optimization functions can be added to the control module.

III. MODELING OF BATTERY CELLS

An accurate battery cell model is needed in order to validate the proposed design by simulation studies. Moreover, monitoring, control, protection, and optimization of the battery systems also need an accurate battery cell model for SOC tracking, etc. In this paper, a hybrid battery

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**Fig. 3.** A switch implementation for Cell $k$ ($k = 1, \ldots, n$) using an n-channel power MOSFET and a p-channel power MOSFET with the gate drive circuit and the control module.

**Fig. 4.** Schematic diagram of the control module.

**Fig. 5.** The cell voltage monitoring circuit.

**Fig. 6.** The electrical circuit-based hybrid battery cell model.
model, as shown in Fig. 6, is used, which combines an electrical battery cell model [11] with a kinetic battery cell model (KiBaM) [12]. The former can accurately capture the I-V characteristics and transient response of a battery cell; while the latter can compensate for the nonlinear effects, such as the recovery effect and rated capacity effect, of the battery cell. Therefore, the hybrid model can accurately predict the runtime and I-V performance of the cell while taking into account the effects of temperature and capacity fading on the cell dynamics. A self-discharge resistor ($R_{\text{self-discharge}}$), a variable capacitor ($C_{\text{capacity}}$), and a current-controlled current source are used for SOC tracking and runtime prediction. A voltage-controlled voltage source is used to bridge the SOC to the battery open-circuit voltage ($V_{\text{oc}}$). The open-circuit voltage, internal series resistance ($R_{\text{series}}$), and resistor-capacitor (RC) network emulate the I-V characteristics and transient response of the cell.

Fig. 7 shows the implementation of the hybrid battery cell model in MATLAB/Simulink. The rated & recovery charge unit offers the nonlinear capacity variation by changing the value of $C_{\text{capacity}}$ during battery operation. All circuit components are implemented by using the standard modules from the SimPowerSystems toolbox.

IV. SIMULATION RESULT

A six-cell battery bank is built by using the proposed design and is simulated in MATLAB/Simulink. Each cell is a 3.7-V, 860-mAh lithium polymer cell, which is modeled by the battery model in Section III. Assuming the initial SOCs of Cells 2-5 are all at 100%, Fig. 8 compare the total energy in Wh that can be supplied by the fixed-configuration, six-cell battery and the proposed self-reconfigurable, six-cell battery for different SOCs of Cells 1 and 6. These results clearly show that the proposed design significantly improves the energy usage of this multicell battery. For example, when the SOC of Cell 1 or 6 or both becomes zero, the whole

![Fig. 7. The hybrid single cell model implemented in MATLAB/Simulink.](image)

![Fig. 8. Total energy in Wh that can be supplied by (a) the fixed-configuration, six-cell battery; (b) the proposed self -reconfigurable, six-cell battery.](image)
battery with the fixed configuration has to be cut off and cannot supply any energy to the load although the usable capacity of the battery is still significant. On the other hand, the proposed self-reconfigurable battery can supply energy from other cells even the SOC of one or both of Cells 1 and 6 becomes zero. For example, in the worst case when the SOCs of Cells 1 and 6 are both zero, the reconfigurable battery can still supply totally 12.728 Wh energy from Cells 2-5, which is 66.7% of the maximum energy capacity of the battery system.

V. EXPERIMENTAL RESULTS

The six-cell battery used in simulation studies is constructed in hardware to further validate the proposed design by using the polymer lithium-ion battery cells. Fig. 9 illustrates the experimental setup. The cells are charged by a variable DC source and discharged through a programmable DC electronic load, which offers Constant Resistor (C.R), Constant Current (C.C), and Pulse Current (P.C) modes. Twelve high-efficiency power MOSFETs are used to form the cell switching circuit on a printed circuit board (PCB). The sensing, control and protection functions are also implemented on the PCB. The sensed information is used for real-time control and protection of the battery pack. Experiments are performed at different scenarios to compare with the corresponding simulation results to validate the proposed design. The results are listed in Table 1. For all scenarios, the experimental results agree with the simulation results. Figs. 10 and 11 compare the terminal voltage responses of the self-reconfigurable, six-cell battery obtained from simulations and experiments for Scenarios 1 and 3, respectively.

In Scenario 1, the six cells are discharged using the C.R. mode simultaneously. Since the initial SOCs of the cells are different, the cells are fully discharged sequentially. Once a cell is fully discharged, it will be disconnected from the battery pack by the cell switching circuit but the remaining cells still provide energy to the load. The results show that not only the steady-state but also the dynamic responses of the battery obtained from simulations agree with those from experiments. Therefore, the comparison in Fig. 8 is effective to validate the superiority of the proposed self-reconfigurable battery over the traditional fixed-configuration battery.

In Scenario 2, all six cells are discharged simultaneously using the C.C. method. In Scenario 3, the six cells are divided into two groups and each group has three cells. The two groups of cells are discharged alternatively, i.e., pulsed discharge (P.C.), with a time interval of 300 s until all the cells are fully discharged. Fig. 11 shows that by using the cell switching circuit, the self-reconfigurable, six-cell battery provides energy to the load with the desired terminal voltage of ~12 V during operation. As shown in Table 1, compared to using the C.C. discharge (Scenario 2), more energy (600 mWh) is supplied by the self-reconfigurable, six-cell battery when using the P.C. discharge (Scenario 3). This P.C. discharge method utilizes the recovery effect to improve the energy conversion efficiency of the battery cells, which however cannot be achieved by the traditional fixed-configuration battery design.
Vth using the proposed design.

Remarkably improved energy usage of multicell battery system. Simulation and experimental results have shown a can be readily added to each cell and the overall battery monitoring, control, protection, and optimization functions in the battery. By using the proposed design, additional operating time and lifespan and enhancing the reliability of failures of single or multiple cells, thereby maximizing the can maximally utilize the battery’s capacity and is tolerant to high-efficiency semiconductor devices. The proposed design in independently. The switching circuit and the control circuit battery only uses two switches to fully control its operation reconfigurable, multicell battery design. A new switching circuit and the control circuit (Fig. 3): R1 = 10 kΩ, R2 = 10 MΩ, R3 = 1 MΩ, R4 = 200 Ω, R5 = 10 kΩ, R6 = 10 kΩ, R7 = 100 kΩ, R8 = 10 KΩ.

VI. Conclusion

This paper has presented a novel series-connected, self-reconfigurable, multicell battery design. A new switching circuit topology has been proposed where each cell in the battery only uses two switches to fully control its operation independently. The switching circuit and the control circuit for each cell have been designed and implemented by using high-efficiency semiconductor devices. The proposed design can maximally utilize the battery’s capacity and is tolerant to failures of single or multiple cells, thereby maximizing the operating time and lifespan and enhancing the reliability of the battery. By using the proposed design, additional monitoring, control, protection, and optimization functions can be readily added to each cell and the overall battery system. Simulation and experimental results have shown a remarkably improved energy usage of multicell batteries using the proposed design.

Appendix

The parameters of the components used in the simulation and experimental studies are listed as follows.

1) Battery cell: pl-383562 2C (polymer lithium-ion); nominal voltage: 3.7 V; nominal capacity: 860 mAh; discharge cutoff voltage (V_disch): 3 V; charge cutoff voltage (V_Over): 4.2 V; maximum discharge current: 2C.

2) Power MOSFET: n-Channel MOSFET; FDP8441 (S41, S42, S51, S52, S61, and S62), V_DSS = 40 V, R_DS(on) = 2.1 mΩ, STD90N02L (S31, S21, and S11), V_DSS = 25 V, R_DS(on) = 5.2 mΩ, V_T = 1.8 V; p-Channel MOSFET: AO403(S32, S22, S12), V_DSS = -30 V, R_DS(on) = 6.2 mΩ, V_T = -2.5 V.

3) Small-signal transistor: MMBT 2222, MMBT 2907.

4) Resistors in the power MOSFET gate drive and control circuit (Fig. 3): R1 = 10 kΩ, R2 = 10 MΩ, R3 = 1 MΩ, R4 = 200 Ω, R5 = 10 KΩ.

Table 1: Comparison of simulation and experimental results

<table>
<thead>
<tr>
<th>Scenario</th>
<th>Discharge method</th>
<th>Cell condition expressed by SOC [%]</th>
<th>Energy [Wh]</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Cell 1</td>
<td>Cell 2</td>
</tr>
<tr>
<td>1</td>
<td>C.R. = 100 Ω</td>
<td>100</td>
<td>80</td>
</tr>
<tr>
<td>2</td>
<td>C.C. = 860 mA</td>
<td>100</td>
<td>100</td>
</tr>
<tr>
<td>3</td>
<td>P.C. = 860 mA</td>
<td>100</td>
<td>100</td>
</tr>
</tbody>
</table>

In the worst scenario where all the cells are operated at the rated voltage of 3.7 V and the maximum current of 1.6 A, the conduction loss of the power MOSFETs in the six-cell battery pack is approximately 64 mW and the total power loss of the gate drive and control circuits is approximately 200 mW. Therefore, the efficiency of the battery system is higher than 98% and can be further improved by using higher-efficiency devices.

References